

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kalnitsky
Applicant's Ref: 59305-8072.US01
(MAX1P072)
Serial No.: 09/882,538



Filed: June 15, 2001

Title: METHOD FOR FORMING A SUPER
SELF-ALIGNED HETERO-JUNCTION
BIPOLAR TRANSISTOR

Examiner: Dana Farahani

Group Art Unit: 2814

Date: October 30, 2002

Confirmation No.: 8283

#9/Pre.
B
marsh
11/10/02

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on October 30, 2002.

Signed: Valerie Peterson

Valerie A. Peterson

AMENDMENT AND RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents

Washington, D.C. 20231

RECEIVED
NOV - 7 2002
TC 2800 MAIL ROOM

Dear Sir:

In response to the Office Action dated July 30, 2002, please enter the following amendments and consider the following remarks.

In the Claims

Please amend the claims as follows. Please cancel claims 6, 13 and 22 without prejudice. A marked up version of the claims, showing the changes made, is attached to this communication at Appendix A.

59305-8072.US02 (MAX1P072)/BRC/JPK
09/882,538

1